

## CALL FOR GRANT APPLICATIONS

(AE2026-0094)

INESC TEC is now accepting grant applications to award 1 Research Grant (BI) within the scope of the Multiannual Funding of R&D Units 2025-2029, with the reference UID/50014/2025, Funded by national funds through the Portuguese Foundation for Science and Technology (FCT), I.P.

### 1. GRANT DESCRIPTION

**Type of grant:** Research Grant (BI)

**General scientific area:** ENGINEERING

**Scientific subarea:** Electrical engineering

**Area of Work:** Electrical engineering

**Grant duration:** 6 months, starting on 2026-07-01, with the possibility of being renewed until the end of the project.

**Scientific advisor:** Justino Miguel Rodrigues

**Workplace:** INESC TEC, Porto, Portugal

**Maintenance stipend:** € 1359.64, [according to the table of monthly maintenance stipend for FCT grants](#), paid via bank transfer. Grant holders may be awarded potential supplements, according to a quarterly evaluation process (Articles 19, 21 and 22 of the [Regulations for Grants of INESC TEC](#) and Annex II), up to a maximum limit of 50% of the monthly maintenance stipend.

INESC TEC supports costs with registration, enrolment or tuition fees, during the grant duration, under the terms established in the internal document: [Payment of Tuition fees to grant holders](#).

The grant holder will benefit from health insurance, supported by INESC TEC.

### 2. OBJECTIVES:

The x-Energy Lab – SmartGrids and Electric Vehicles integrates the Centre for Power and Energy Systems (CPES) at INESC TEC. Its main mission is to test and validate innovative technologies within the general context of the decarbonization of the economy, with emphasis on integration in electric power grids, more specifically: a) Smart Grids: Integration and management of distributed energy resources, energy management, new architectures for electric power grids, among others; b) Electric Mobility: Integration of electric vehicles into power grids, development of management and charging technologies for electric vehicles, among others; c) Active Consumption Management: Integration of management and optimization solutions for controllable loads in residential and commercial buildings, interoperability.

Within this scope, this research grant aims to achieve the following objectives:

- Enhance the x-Energy Lab capabilities to support R&D activities within the context of CPES;

- Ensure operational readiness of existing laboratorial test infrastructure;

This research grant offers a unique opportunity to join a leading research team and contribute to the advancement of more efficient, secure, and sustainable energy solutions. If you are passionate about being part of this transformation and making an impact on the future of power grids, join us!

### 3. BRIEF PRESENTATION OF THE WORK PROGRAMME AND TRAINING:

- Understand the current state of laboratory infrastructure and receive training related to infrastructure operation.
- Understand the current laboratory capabilities to support R&D in the context of CPES activity.
- Identify potential enhancements in infrastructure management and operation procedures, as well as potential reinforcements, suitable to enhance laboratory capabilities to support R&D in the context of CPES activity.
- Write a report describing the developed work, and the main conclusions and recommendations.
- In the scope of laboratory infrastructure enhancement, contribute to the development of prototypes. This activity is transversal to all others.
- Support R&D activities taking place in the x-Energy laboratory and ensure the operational readiness of the necessary laboratorial test beds. This activity is transversal to all others

### 4. REQUIRED PROFILE:

#### Admission requirements:

Master's Degree in Electrical Engineering

The awarding of the fellowship is dependent on the applicants' enrolment in study cycle or non-award courses of Higher Education Institutions.

#### Preference factors:

Academic background or previous experience related to electrical installations, automation, or electronics;  
Experience with relevant laboratory equipment; Knowledge of programming in Python, C, or C++.

#### Minimum requirements:

Previous academic background in applied electrical engineering and automation.

### 5. EVALUATION OF APPLICATIONS AND SELECTION PROCESS:

**Selection criteria and corresponding valuation:** the first phase comprises the Academic Evaluation (AC), based on the criteria referred to in Article 12 of the [Regulations for Grants of INESC TEC](#), while the second phase comprehends the Individual Interview (EI). All factors are evaluated on a scale of 0 to 100, taking into account the applicants' merit, suitability and conformity with the preference factors.

The weight of the AC factors are as follows: Academic Qualifications (FA, 40%), Scientific Publications (PC, 10%), Experience (EX, 40%) and Motivation Letter (CM, 10%).

Candidates who score less than 50 points in the AC average will be considered excluded on absolute merit. The top five candidates approved on absolute merit will be qualified for the individual interview. The Final Grade (CF) is obtained by the weighted average of AC (90%) and EI (10%).

### **DISABILITY INCENTIVE**

Candidates who present a degree of disability equal to or greater than 90% will benefit from an incentive (20) in the score of the CV Assessment.

Candidates who present a degree of disability equal to or greater than 60% and less than 90% will also benefit from an incentive (10) in the score of the CV Assessment.

Said score may, in these cases, exceed 100 points.

Candidates must demonstrate the degree of disability during the application, namely through the submission of the Multi-Purpose Medical Certificate of Disability, issued in accordance with Decree-Law no. 202/96, of October 23 currently in effect.

Candidates must declare, in the application form, the type of disability used throughout the selection process, in order to proceed with the required adaptations.

### **The Selection Jury is composed of the following members:**

President of the Jury: Justino Miguel Rodrigues

Full member: Ahmed Hussein

Full member: Rui Esteves Araujo

Substitute member: Cleberton Reiz

**Release of results and prior hearing:** the results of the selection process, as well as the terms and procedures for prior hearing, will be released to the applicants by email, under the terms referred to in Article 13 of the Regulations for Studentships and Fellowships of INESC TEC.

## **6. FORMALISATION OF APPLICATIONS:**

### **Application Documents:**

1. Motivation letter;
2. Curriculum Vitae (must include the list of previous fellowships, their type, beginning and end dates, funding entities and host institutions);
3. Certificate or diploma degree;
4. Proof of enrollment in a degree awarding study cycle or in a non degree awarding Higher Education program.
  - The proof of enrollment may be presented just during the grant hiring stage.
5. Signed declaration stating the infringement of the grant holder's duties (article 14, no. 4)
6. Documental evidence to support the country of residence, residence permit or other legally equivalent document, in cases where the applicant is a foreigner or non-resident in Portugal - valid until the beginning of the grant.
7. Other supporting documents relevant to the final assessment.

Failure to deliver the required documents within the 90-day period after the date of the notice of the conditional awarding of the grant implies its cancellation.

**Application period:** From 2026-05-01 to 2026-05-15

**Submission of applications:** the application will be formalised by submitting the form available in the *Work With Us* section of INESC TEC website.

## **7. BINDING LEGISLATION AND REGULATION**

The hiring process shall comply with the current legislation regarding the Research Grant Holder Statute, approved by Law no. 40/2004 of August 18, in its current wording, as well as by the [Regulations for Grants of INESC TEC](#) and for [FCT Grants Regulation in force](#).

For more information, please check the [Regulations for Grants of INESC TEC](#) and relevant annexes at [www.inesctec.pt/bolsas](http://www.inesctec.pt/bolsas)

